

# Yufei Yan

EMAIL: [yzy0050@auburn.edu](mailto:yzy0050@auburn.edu) LINKEDIN: [www.linkedin.com/in/yufei-yan](http://www.linkedin.com/in/yufei-yan)  
WEBPAGE: [www.auburn.edu/~yzy0050](http://www.auburn.edu/~yzy0050) PHONE: 765-760-7812 GITHUB: Yufei-Yan

## EDUCATION

**Auburn University**, Expected Graduation: 05/2020 08/2015-Present  
Major: Computer Science, Degree: Ph.D., GPA: 3.58  
**Xi'an University of Posts and Telecommunications** 09/2007-07/2012  
Major: Telecommunications 09/2007-07/2009  
Major: Computer Science, Degree: Bachelor 09/2011-07/2012  
**Ball State University** 08/2009-08/2011  
Major: Computer Science, Degree: Bachelor, GPA: 3.702

## EMPLOYMENT

**Auburn University**, *Graduate Research Assistant* 05/2016-Present  
**Auburn University**, *Graduate Teaching Assistant* 08/2015-06/2016  
- COMP6360 Wireless and Mobile Networks 01/2017-05/2017 & 01/2016-05/2016  
- COMP4320 Computer Networks 08/2016-12/2016  
- COMP2710 Software Construction 08/2015-12/2015  
**Inner Mongolia Jincai Information Ltd.** *SWE* 12/2014-02/2015  
Worked on a financial system project as a contractor. The system was serving transactions between Treasure Department and commercial banks. I worked on back-end, writing in C language to parse transaction messages.  
**Huawei Technology, Terminal Company**, *SWE* 08/2012-06/2014  
During the time I worked for Huawei Technology after I got my bachelor's degree, I worked in Mobile Broadband Development Department, implementing 3GPP radio protocol and analyzing problems regarding the protocol stack for mobile Wi-Fi products and LTE embedded module products. I developed web SDK for HiLink series of products with RESTful API, interfacing front-end and back-end on the chip; analyzed GSM, WCDMA and LTE radio protocol problems; analyzed routing protocol problems and MBIM protocol problems; worked on Qualcomm, Intel Infineon and Hi-Silicon chipset platform.  
- GL10P/301HW & HWD14 01/2014-06/2014  
- Global Operators Technical Access 09/2013-01/2014  
- Embedded Module-ME906x Series & MU736 03/2013-09/2013  
- HiLink Datacard 08/2012-03/2013

## PROJECTS

**Platooning System**, 05/2017-Present  
Link: <http://eng.auburn.edu/news/2017/10/truck-platooning-demo.html>  
Currently working on a TARDEC truck platooning V2V communication project, which involves Computer Science, Mechanical departments and Bosch Inc. The project has made me gain a good understanding of DSRC protocol and SAE J2735 standard: 1. I designed software interface for truck controller and WAVE transmitter; 2. Develop software for transmitting messages between DSRC radios; 3. Research on porting OLSR for WSMP; 4. Research on multiple routing protocols for efficient multiple truck message routing.  
**AUTTC Website** 02/2017-05/2017  
The website is for Auburn University Table Tennis Club: 1. Designed the website architecture, including back-end and front-end architecture; 2. Implemented back-end in Java using JSP; 3. Designed database in MySQL.  
**Indoor Navigation System** 09/2015-05/2017  
1. Driver development for Atheros AR928X to get TOA value on Intel IoT products. 2. User space development for efficiently calculating distances in C, Python and Go.

## SKILLS

Solid background in Computer Networks, Linux C Programming, 2G/3G/LTE radio protocol analysis.  
Strong skills in computer programming, including C/C++, Java, Python.  
Ability to analyze series of network problems with various tools, including Qualcomm QXDM, Wireshark, tcpdump, etc.  
Familiar with Linux kernel development.  
Experienced on IoT devices, such as Intel Galileo and Edison, Gateworks Single Board, Raspberry Pi, etc.  
Strong willing and skills to communicate and teamwork.